

METHOD AND APPARATUS FOR OUTPUTTING A DATASTREAM PROCESSED
BY A PROCESSING DEVICE

ABSTRACT OF THE DISCLOSURE

To synchronize a decoder at the receiving end, particularly an MPEG audio
5 decoder, with the corresponding coder operating at the transmitting end, it is proposed
to supply the data decoded by the decoder to a FIFO memory, the output clock rate at
which the data are read out or output from the FIFO memory, being adjusted as a
function of the loading level of the FIFO memory.

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